

Notice of References Cited		Application/Control No.	Applicant(s)/Patent Under Reexamination URBAN ET AL.	
		10/676,295	Examiner Shamim Ahmed	
			Art Unit 1765	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,060,329	05-2000	Kamata et al.	438/9
*	B	US-6,926,844	08-2005	Laermer et al.	216/67
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N	WO 01/39261	05-2000~	PCT	Laermer et al.	-----
	O		5-31-2001			
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf, Stanley: "Silicon processing For The VLSI ERA: Vol.1, pages 568-569, 1986.
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.